

FIG. 1

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FIG. 2

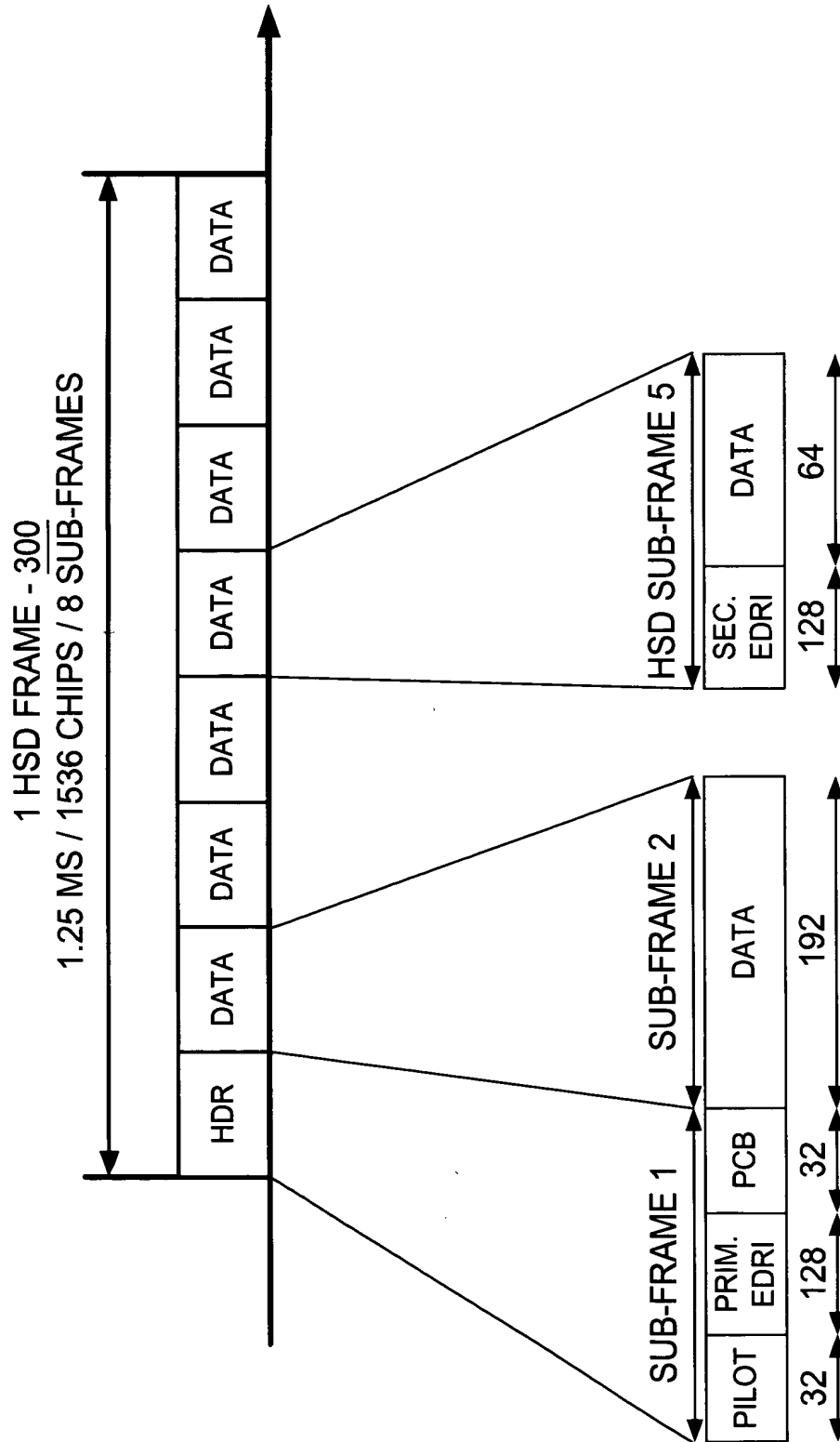


FIG. 3

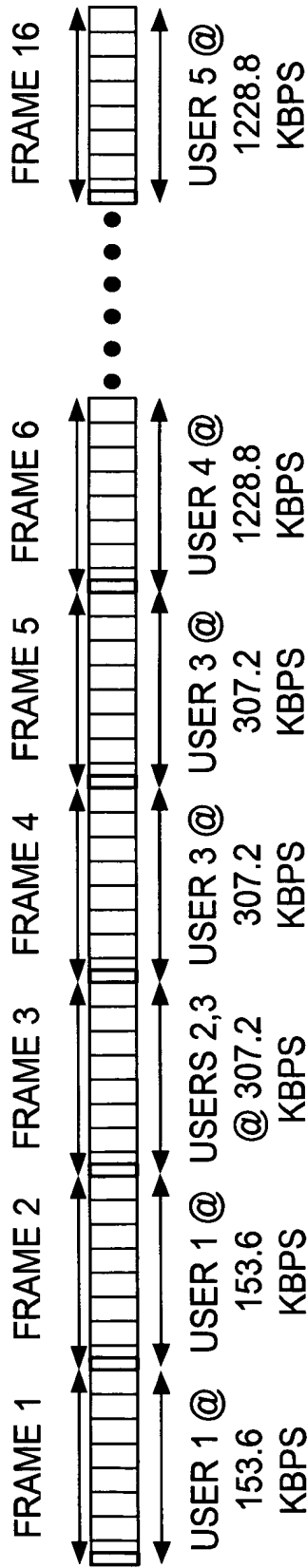


FIG. 4A

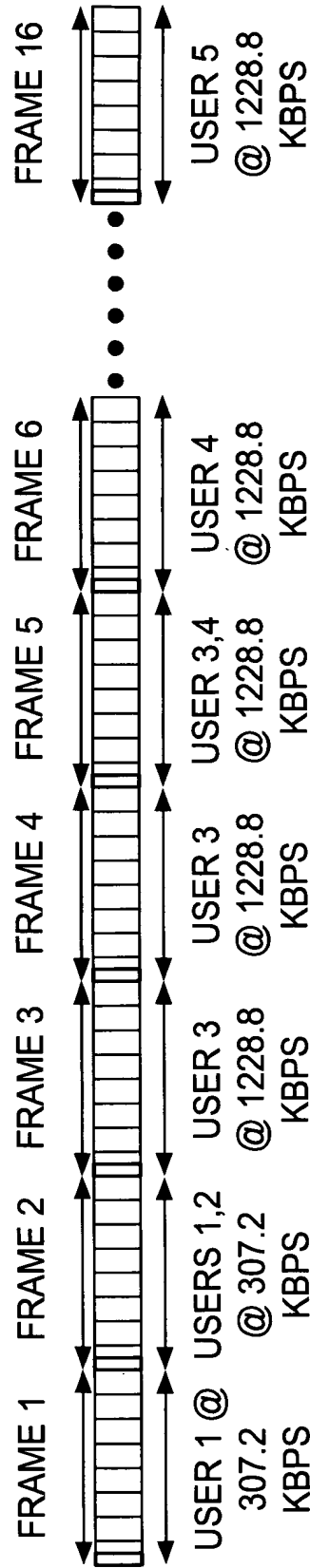


FIG. 4B

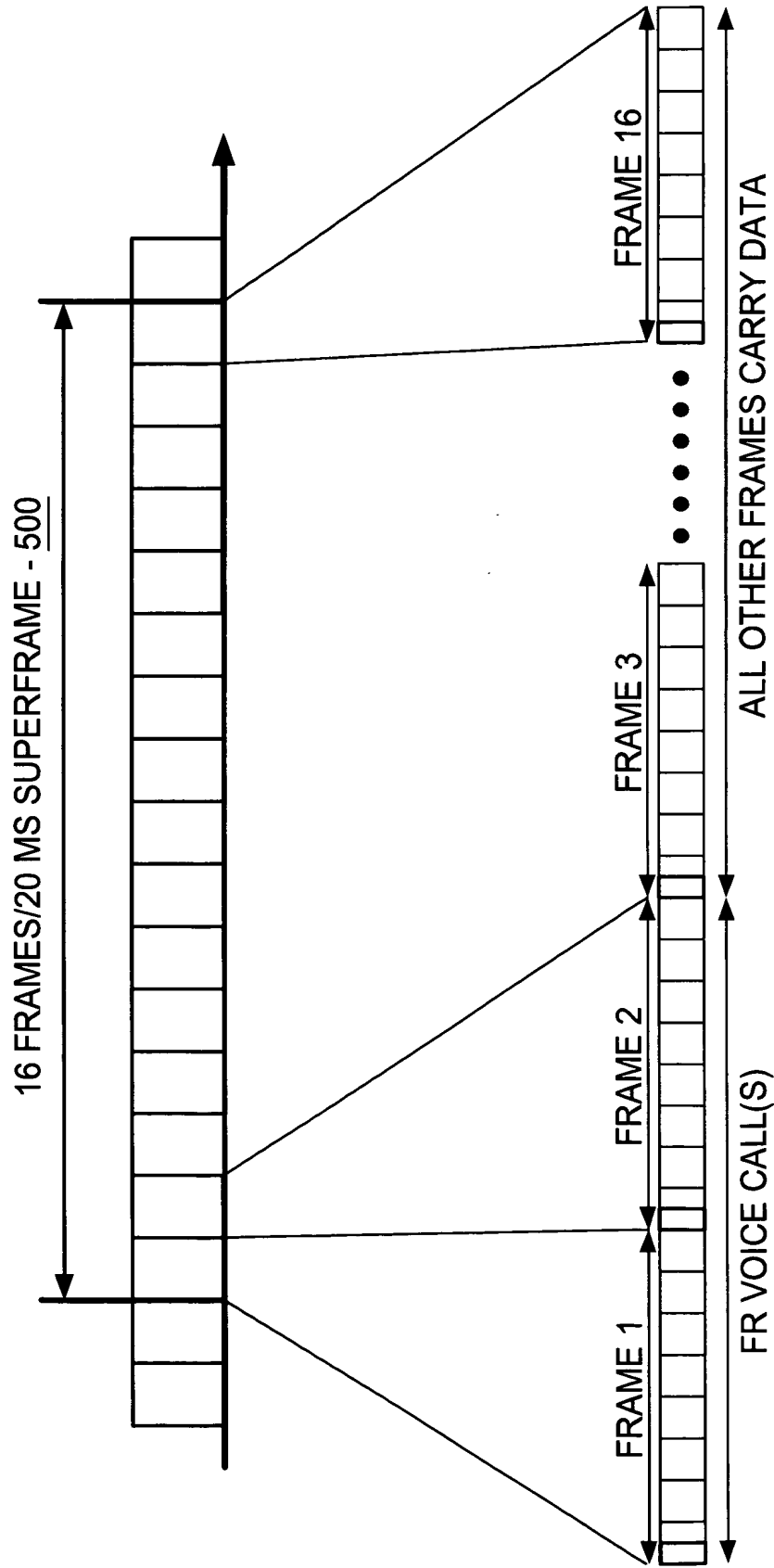


FIG. 5

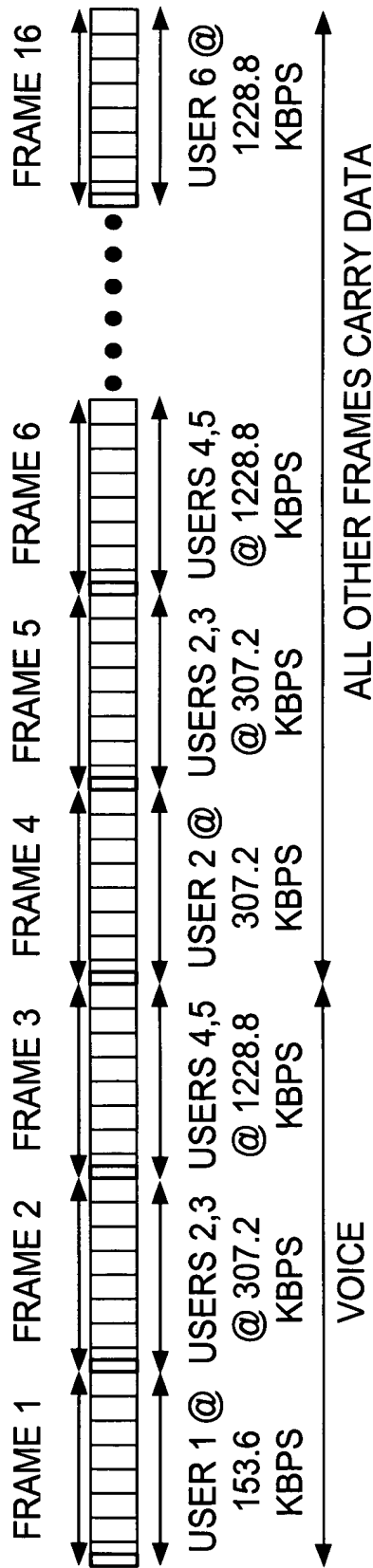


FIG. 6A

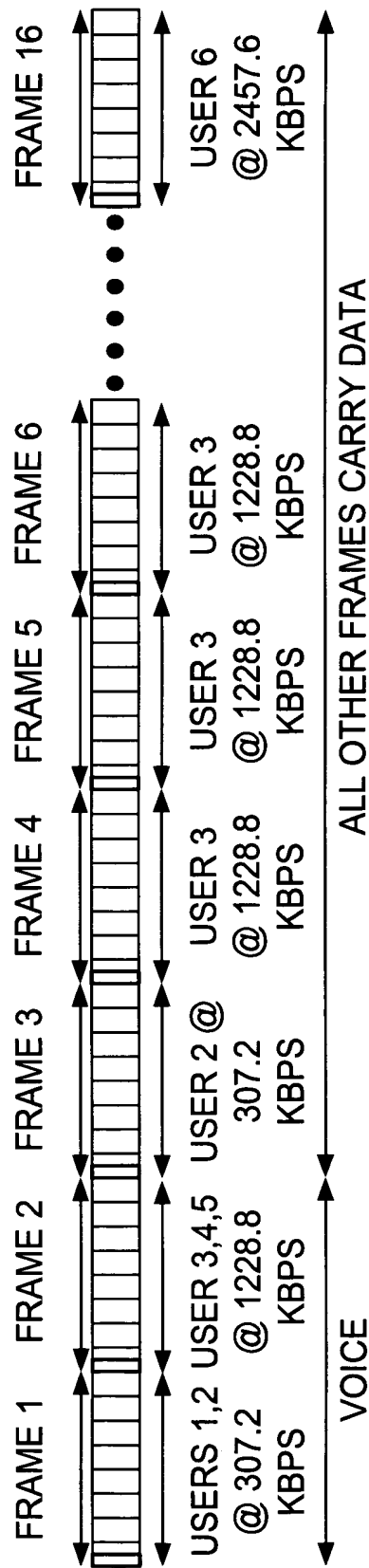
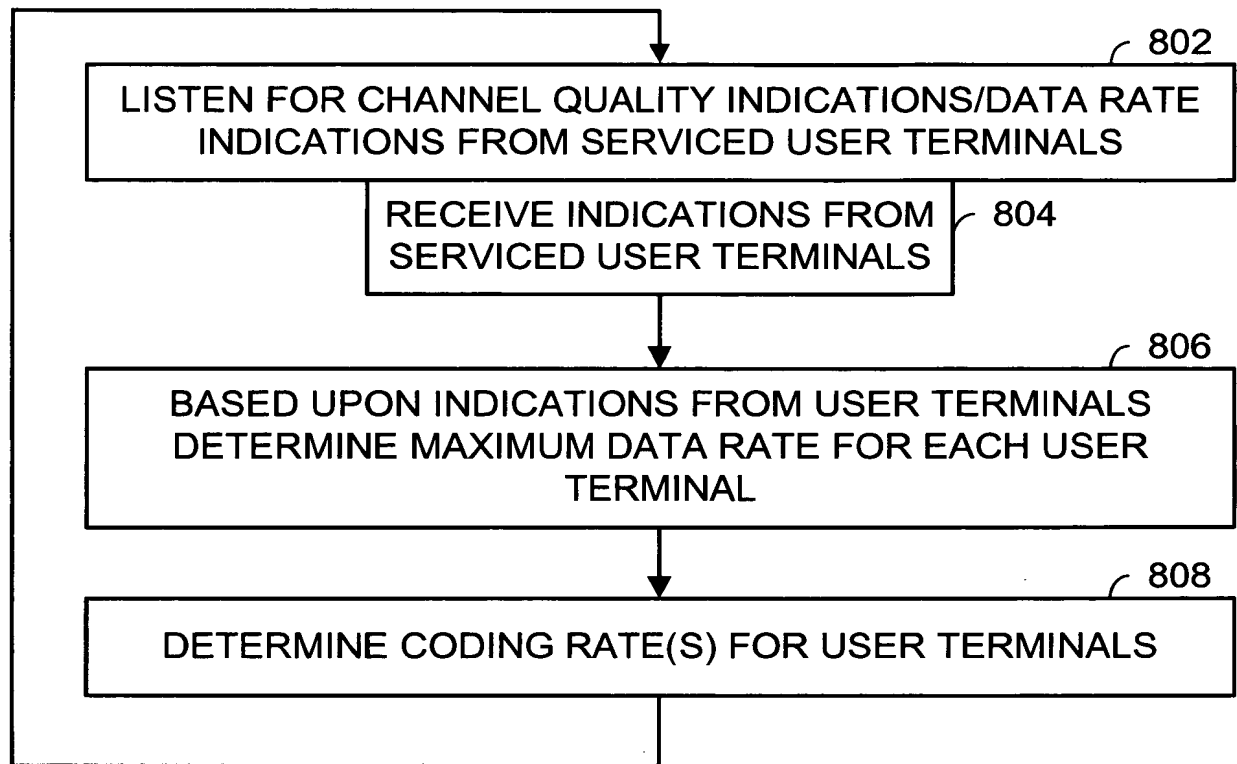


FIG. 6B



**FIG. 8**

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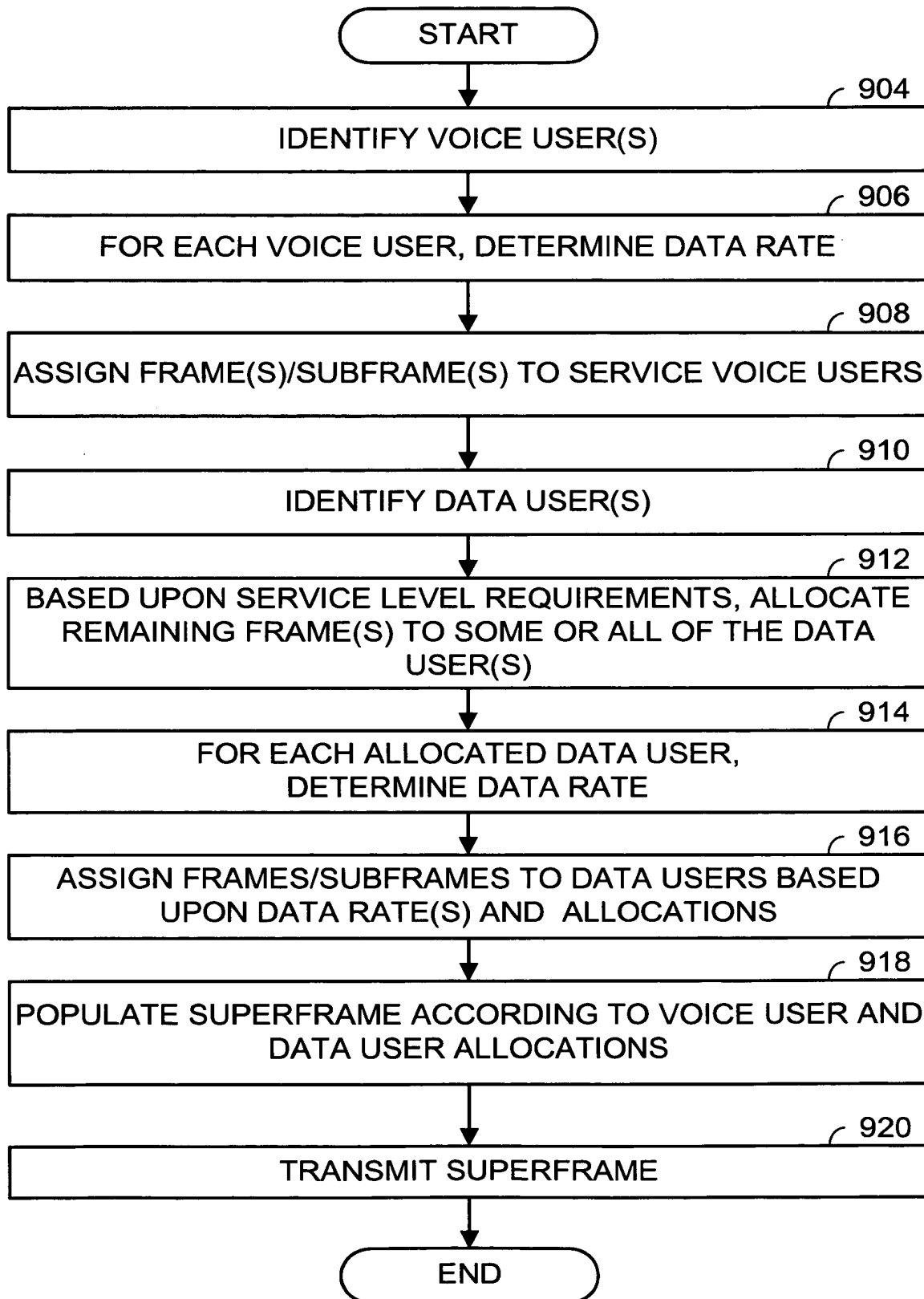


FIG. 9

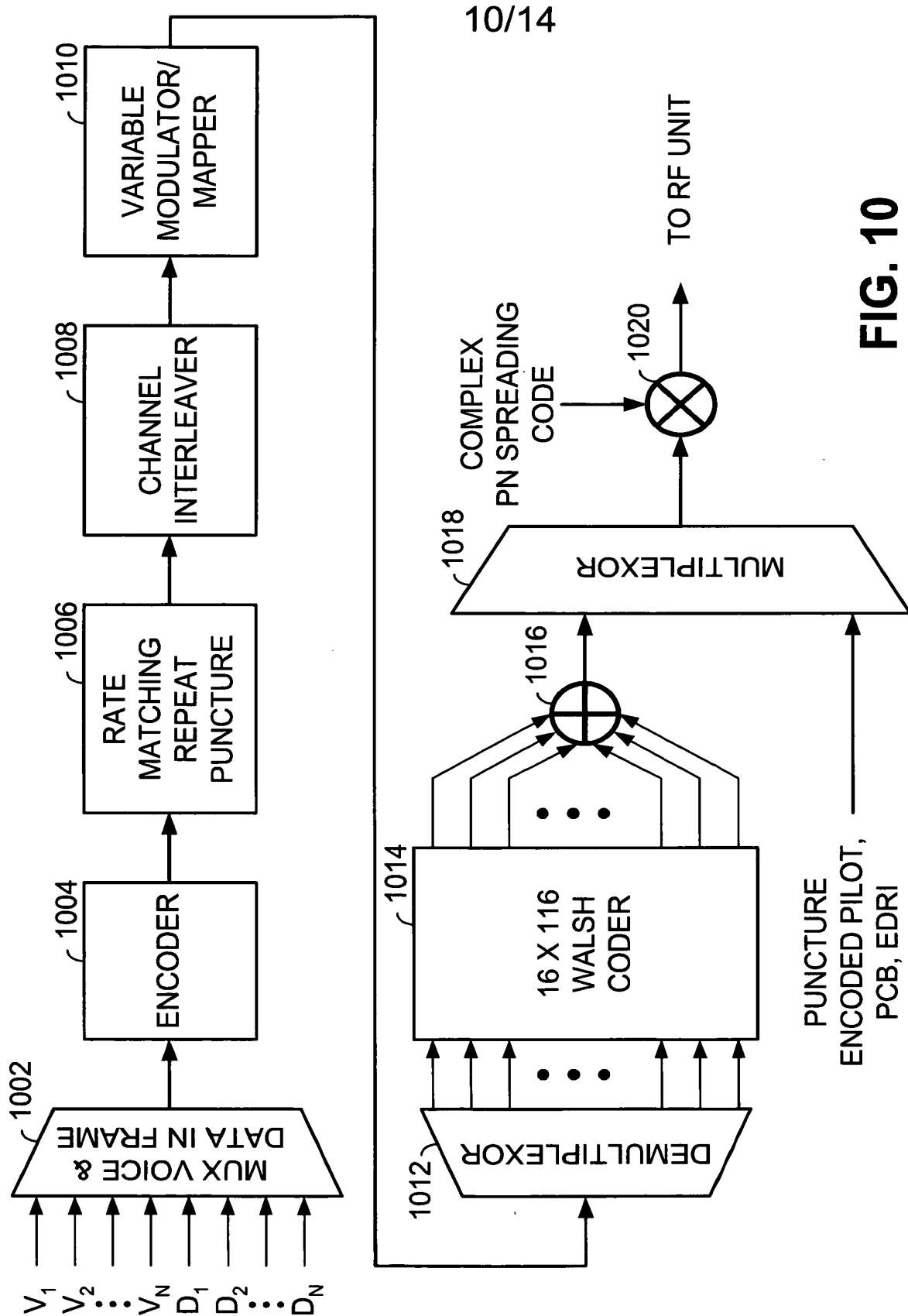


FIG. 10

FIG. 11 is a block diagram of a multi-user communication system. The system includes a plurality of users (USER 1 VOICE/DATA, USER N VOICE/DATA) and a central processing unit. Each user's data is processed by an ENCODER (1104A, 1104N), followed by a RATE MATCHING REP. PUNC. block (1106A, 1106N), a CHANNEL INTERLEAVER (1108A, 1108N), and a VARIABLE MODULATOR/MAPPER (1110A, 1110N). The outputs of these blocks are combined in a MUX (1111). The MUX output is then processed by a DEMUX (1112), which outputs a COMPLEX SYMBOL. This symbol is then processed by a 16 X 16 WALSH CODER (1114), which outputs a PUNCTURE ENCODED PILOT, PCB, EDRI. This signal is then combined with a PN SPREADING CODE (1120) in a MULTIPLEXOR (1118), resulting in a signal sent TO RF UNIT.

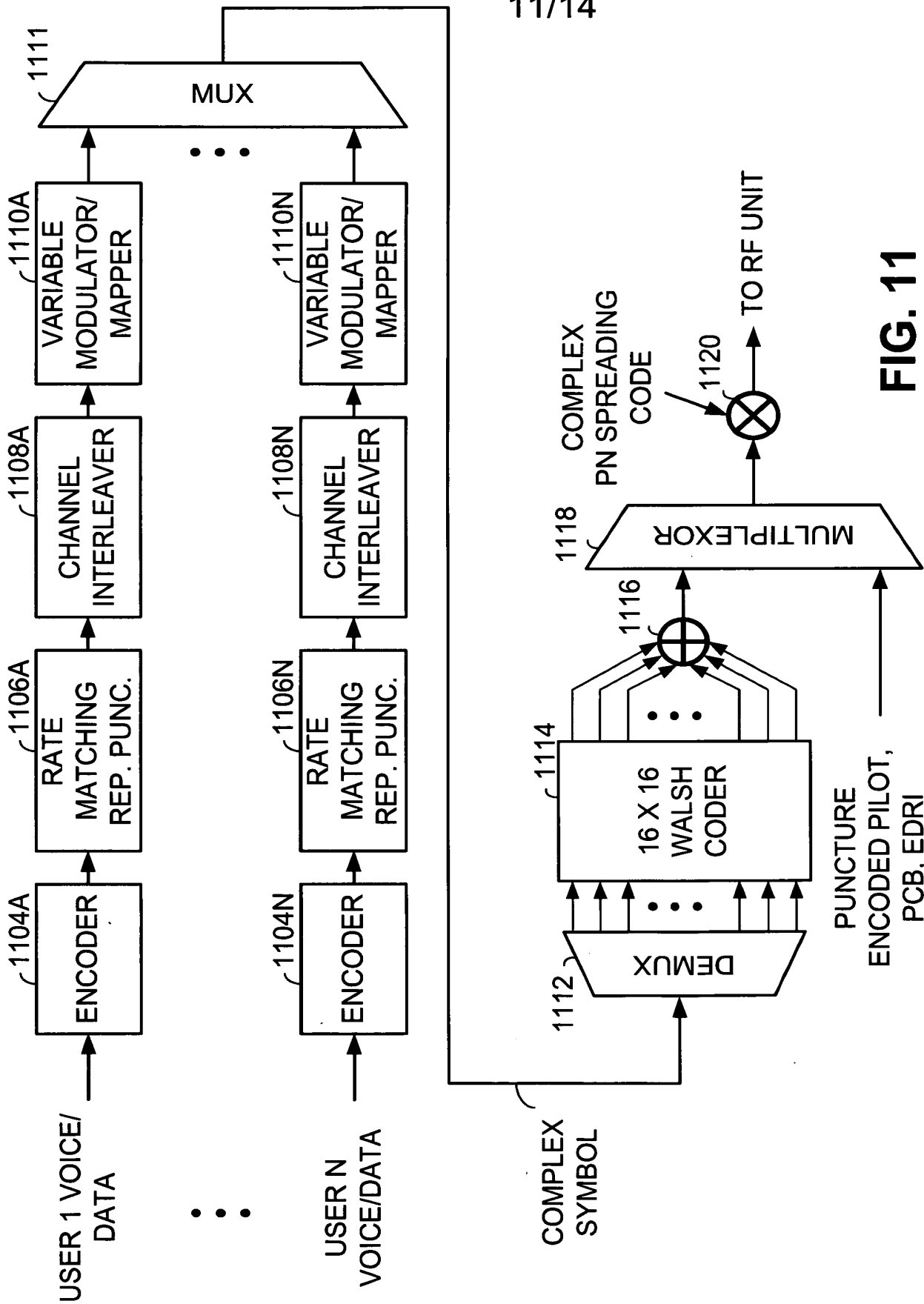


FIG. 11

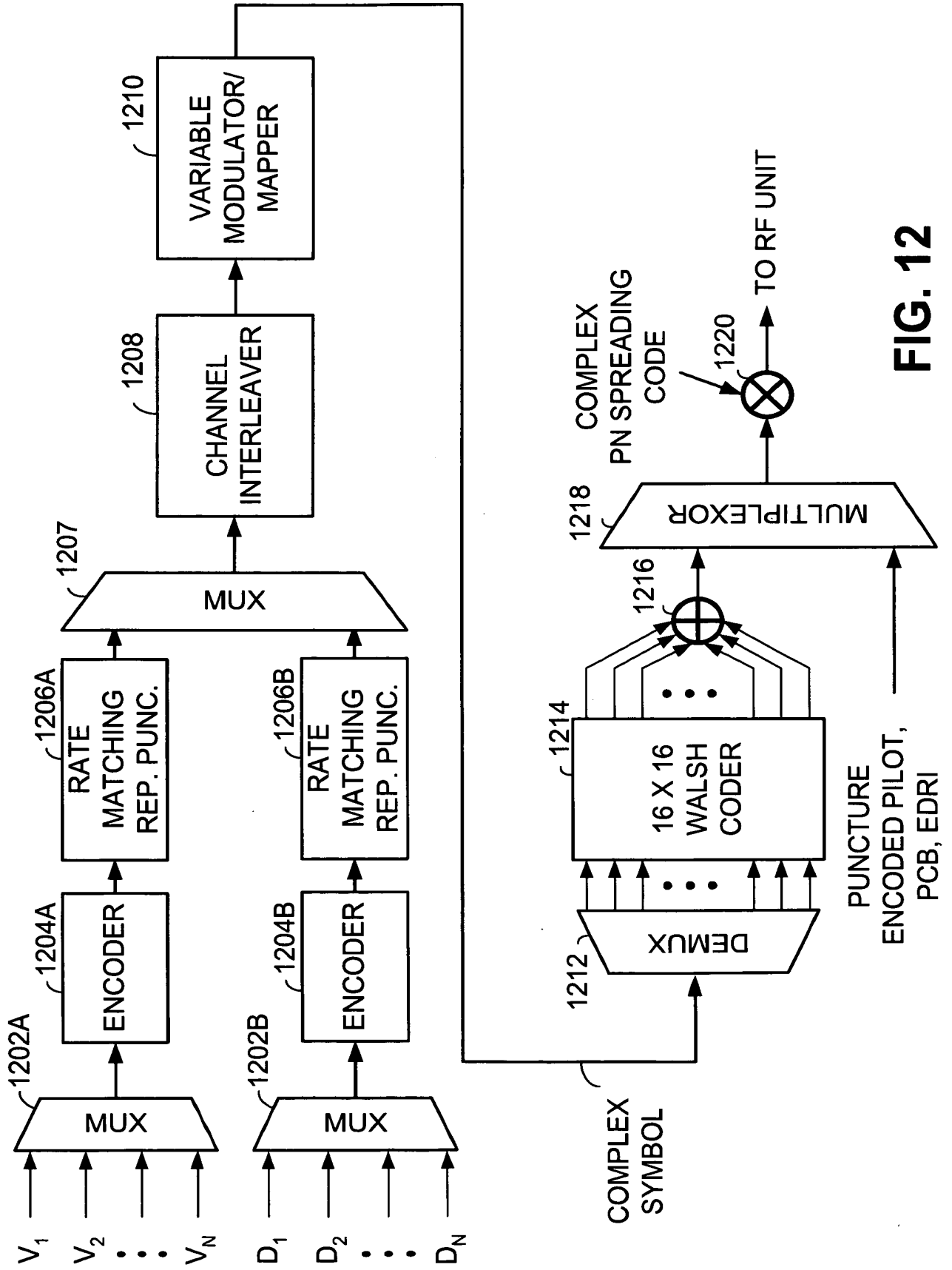


FIG. 12

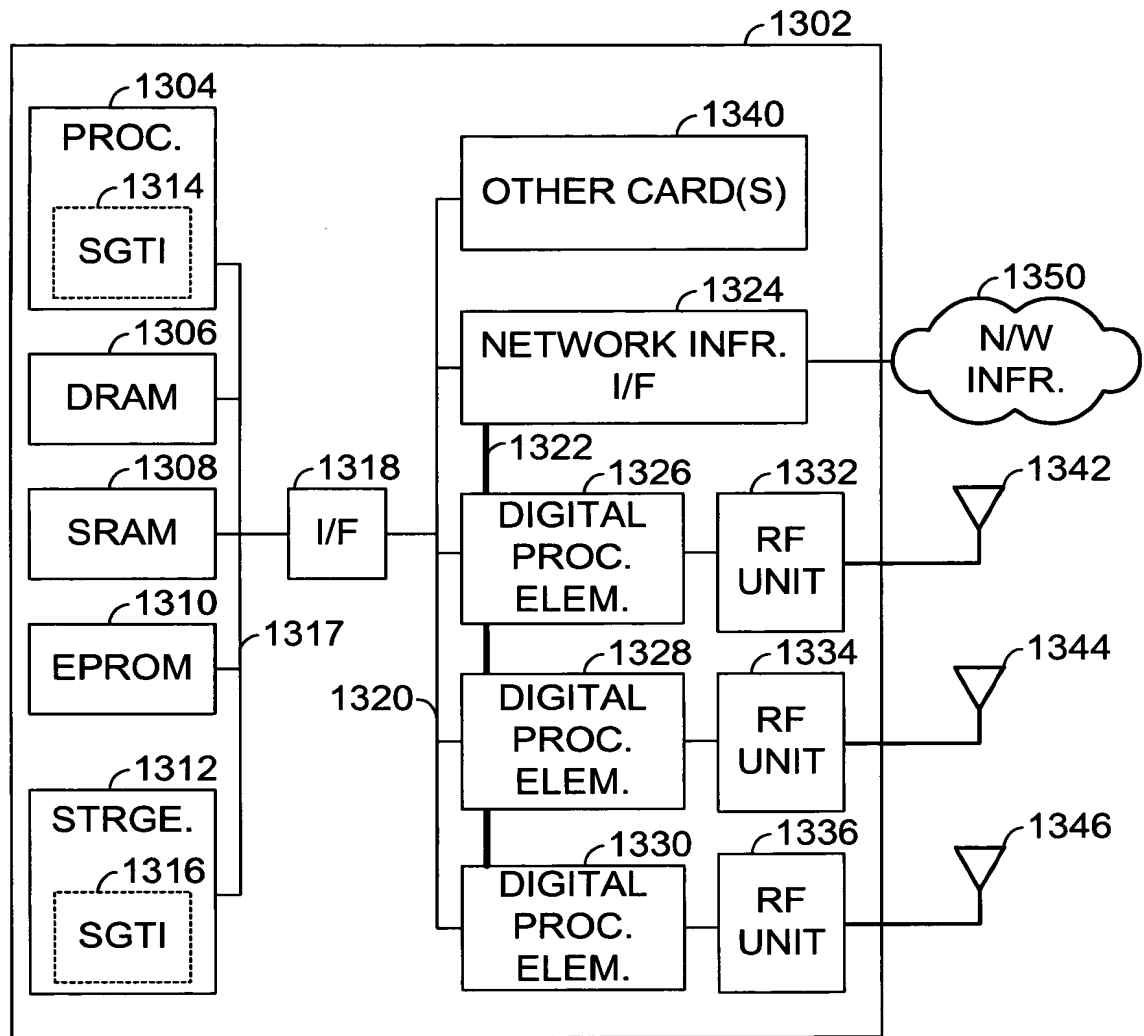


FIG. 13

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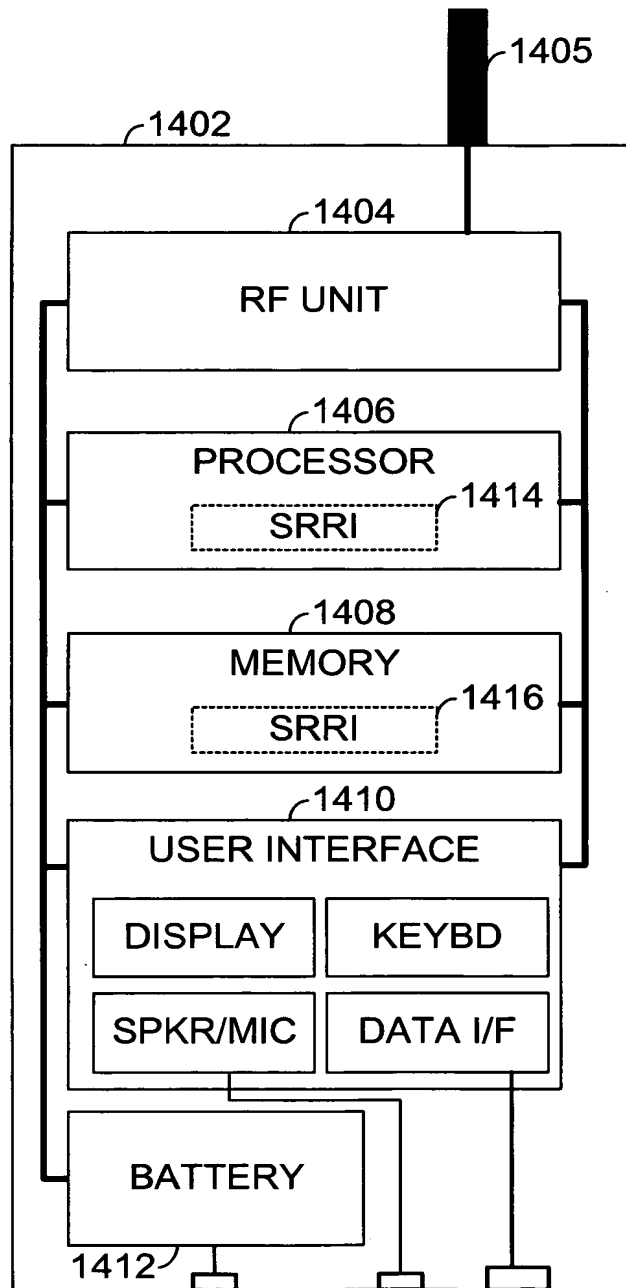


FIG. 14